

8 | 7 | 6 | 5 | 4 | 3 | DWG. NO. | SP. REV. | 1

REVISIONS			
REV	DESCRIPTION	DATE	APPROVED

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C

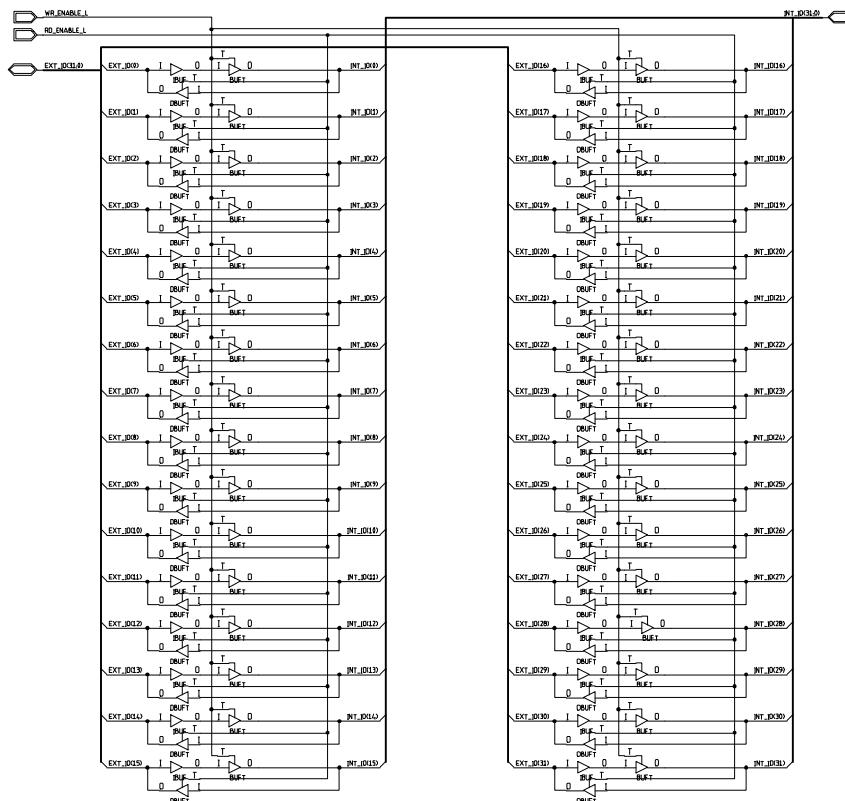
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CONTRACT NO.		Fermi National Accelerator Laboratory
APPROVALS	DATE	XILINX FPGA VMEbus Slave Interface Local Teststand Calibration 32 bit I/O
DRAWN	2/8/96	Theresa Shaw
CHECKED		
ISSUED	SHEET	DWG. NO. TMS-TRAC-020896-P1A REV A 1 of 1
SCALE		

8 | 7 | 6 | 5 | 4 | 3 | DWG. NO. | SP. REV. | 1

2 | 1 | 0 | DWG. NO. | SP. REV. | 1

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